

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended): A digital receiver fast frequency and time acquisition system ~~using a single synchronization word~~ comprising:

a first channel select (CS) filter filtering an incoming digital baseband signal;

a frame synchronization detector for recognizing a single time synchronization word from the first filtered signal;

a coarse symbol time estimator for coarsely determining the symbol time synchronization of the digital signal from the frame synchronization detector;

a one-step fine frequency estimator for finely determining the frequency error of the signal from the coarse symbol time estimator for providing a frequency adjustment signal;

a mixer for combining the incoming digital baseband signal with the frequency adjustment signal and providing a frequency corrected digital signal;

a second CS filter for filtering the frequency corrected digital signal;

a fine symbol time estimator for determining symbol timing with greater precision of the filtered signal from the second CS filter; and

a symbol detector for interpreting the second CS filtered digital signal from the fine symbol time estimator.

2. (Original): A digital receiver fast frequency and time acquisition system as in claim 1, wherein the first CS filter has a wider bandwidth than the second CS filter.

3. (Previously presented): A digital receiver fast frequency and time acquisition system as in claim 1, wherein the second CS filter has exactly half (or any other simple fractions such as one third, or one fourth etc) the bandwidth of the first CS filter.

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4. (Original): A digital receiver fast frequency and time acquisition system as in claim 1, wherein the first CS filter has a 3 decibel (dB) bandwidth of approximately 6 Kilohertz (KHz).

5. (Original): A digital receiver fast frequency and time acquisition system as in claim 4, wherein the second CS filter has a 3 decibel (dB) bandwidth of approximately 3 Kilohertz (KHz).

6. (Currently amended): A fast frequency and time acquisition system for synchronizing digital information for use with a digital radio frequency (RF) receiver comprising:

- a first channel select (CS) filter for filtering digital baseband information;
- a frame synchronization detector for detecting a single synchronization word in the digital baseband information from the first CS filter;
- a coarse symbol time estimator for coarsely determining the symbol time of the digital signal from the frame synchronization detector;
- a one-step fine frequency estimator for finely determining the frequency error of the signal from the coarse symbol time estimator for providing frequency adjustment;
- a mixer for combining the unfiltered digital information with the frequency error estimate to provide a mixed frequency corrected digital signal;
- a second CS filter for filtering the mixed digital signal;
- a fine symbol time estimator for finely determining the symbol time of the signal from the second CS filter; and
- a symbol detector for decoding the digital signal from the fine symbol time estimator.

7. (canceled)

8. (Original): A digital receiver fast frequency and time acquisition system as in claim 6, wherein the first CS filter has a wider bandwidth than the second CS filter.

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9. (Original): A digital receiver fast frequency and time acquisition system as in claim 6, wherein the second CS filter has exactly half (or any other simple fractions such as one third, or one fourth etc) the bandwidth of the first CS filter.

10. (Original): A digital receiver fast frequency and time acquisition system as in claim 6, wherein the first CS filter has a 3 decibel (dB) bandwidth of approximately 6 Kilohertz (KHz).

11. (Original): A digital receiver fast frequency and time acquisition system as in claim 10, wherein the second CS filter has a 3 decibel (dB) bandwidth of approximately 3 Kilohertz (KHz).

12. (Currently amended): A method for rapidly acquiring time and frequency synchronization for a digital RF baseband signal comprising the steps of:

directing a digital baseband signal to a first channel select (CS) filter;

detecting a single time synchronization word in the digital signal from the first CS filter;

determining a coarse symbol time estimate of the signal from the first CS filter;

determining a one-step fine frequency error estimate of the digital signal from the coarse symbol time estimator;

applying a frequency correction to the digital signal to provide a frequency corrected signal via a mixer;

applying the frequency corrected signal to a second CS filter;

determining a fine symbol time estimate of the signal from the second CS filter;

applying a fine time correction to the signal from the second IF filter; and

detecting the symbols in the digital bit/symbol stream from the fine time corrected signal.

13. (Original): A digital receiver fast frequency and time acquisition system as in claim 12, wherein the first CS filter has a wider bandwidth than the second CS filter.

14. (Original): A digital receiver fast frequency and time acquisition system as in claim 12, wherein the second CS filter has exactly half the bandwidth of the first CS filter.

15. (Original): A digital receiver fast frequency and time acquisition system as in claim 12, wherein the first CS filter has a 3 decibel (dB) bandwidth of approximately 6 Kilohertz (KHz).

16. (Original): A digital receiver fast frequency and time acquisition system as in claim 15, wherein the second CS filter has a 3 decibel (dB) bandwidth of approximately 3 Kilohertz (KHz).